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Docket No.: SC12856TP
Applicant: William J. Taylor, Jr. et al.
Serial No.: 10/670,634
Art Unit: 2823
Filed: September 25, 2003
Class: 438-301000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	X	1 page Facsimile Cover Sheet
2.	X	1 page PTOL-85B Issue Fee Transmittal (in duplicate)
3.	X	1 page Issue of Patent to Assignee Pursuant to 37 CFR 3.81
4.	X	2 page Comments on Statement of Reasons for Allowance
5.	X	2 page Amendment under 37 CFR 1.312

Paid by Deposit Account: 503079, Freescale Semiconductor, Inc. $1630

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DOCKET NO. SC12856TP

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT	William T. Taylor, Jr.	GROUP ART UNIT:	2823
APPLN. NO.:	10/670,634	EXAMINER:	William M. Brewster
FILED:	September 25, 2003	CONFIRMATION No.:	6860

TITLE: SEMICONDUCTOR PROCESS FOR DISPOSABLE SIDEWALL SPACERS

Certificate of Transmission under 37 C.F.R 1.8

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office.

on 9-27-04

Pat Thomas
Signature

PAT THOMAS
Printed Name of Person Signing Certificate

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Entrance of the comments provided below regarding the statement of reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.

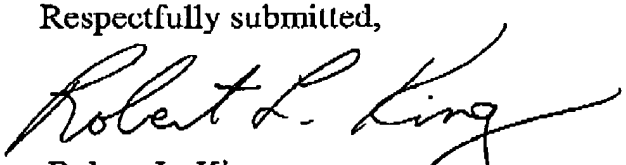
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DOCKET NO. SC12856TP

REMARKS

In a communication mailed August 23, 2004, claims 1- 6-15 and 18-22, renumbered as claims 1-16 were allowed. The Examiner provided reasons for allowance of the claims. Additionally, the Examiner quoted claim recitals from claims 1 and 21. Claim 1 was stated to be allowable because the "forming an insulating layer..." was not found in the prior art of record. In addition to this reason other features of claim 1 as noted in Applicants' previous communication and the combination of claim recitals are not taught or suggested by the prior art of record. Claim 21 was stated to be allowable because "exposing said at least lower dielectric constant" was not found in the prior art of record. In addition to this reason other features of claim 1 as noted in Applicants' previous communication and the combination of claim recitals are not taught or suggested by the prior art of record. Therefore, Applicants' position is that the Examiner's quotation of specific portions of the independent claims was illustrative and not the only points of novelty in the allowed claims.

Respectfully submitted,



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